PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants

Bruno Murari et al.

Filed

November 26, 2003

For

CASCODED POWER AMPLIFIER, PARTICULARLY FOR USE

IN RADIO FREQUENCY

Docket No.

: 856063.758

Date

: November 26, 2003

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicants wish to make known to the U.S. Patent and Trademark Office the references set forth on the attached form PTO-1449. Applicants note that since the application filing date is after June 30, 2003, copies of the cited U.S. patents and published patent applications are not required and accordingly have not been provided. Copies of all other cited references are enclosed. As to any reference cited, applicants do not admit that it is "prior art" under 35 U.S.C. §§ 102 or 103, and specifically reserve the right to traverse or antedate any such reference, as by a showing under 37 C.F.R. § 1.131 or other method. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicants' duty to disclose all information they are aware of which is believed relevant to the examination of the above-identified application, applicants believe that their invention is patentable.

Also included is a copy of a European Search Report from the priority application.

Please acknowledge receipt of this Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Applicants believe this Information Disclosure Statement has been timely filed, however, the Director is authorized to charge any fee due by way of this Information Disclosure Statement to our Deposit Account No. 19-1090.

Respectfully submitted,

Seed Intellectual Property Law Group PLLC

Dennis M. de Guzman Registration No. 41,702

DMD:wt

Enclosures:

701 Fifth Avenue, Suite 6300 Seattle, Washington 98104-7092

Phone: (206) 622-4900 Fax: (206) 682-6031

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Sheet 1 of 1

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE (REV.7-80) PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)					ATTY. DOCKET NO. 856063.758 APPLICANTS Bruno Murari et al. FILING DATE November 26, 2003	GROUP ART UNIT				
			U.S	PATENT 1	DOCUMENTS					
*EXAMINER INITIAL		DOCUMENT NUMBER DATE		NAME		CLASS		SUBCLASS	FILING DATE IF APPROPRIATE	
	AA	5,463,347	10/31/95	Jones et al. Yushan et al.		330		253		
	AB	6,353,345	03/05/02			327		112		
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-	AL	0 790 703	08/20/97	EP						
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OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.) Hong, Merit Y., "Simulation and Fabrication of Submicron Channel Length D Transistors for Analog Applications," IEEE Transactions of Electron Devices, 2230, December 1993.										·2222-
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